

# 2.0A HIGH EFFICIENCY SCHOTTKY BARRIER RECTIFIER

PowerDI®123

### **Features**

- Guard Ring Die Construction for Transient Protection
- Low Power Loss, High Efficiency
- Low V<sub>F</sub> and Low Leakage Current
- Patented Interlocking Clip Design for High Surge Current Capacity
- Lead Free Finish, RoHS Compliant (Note 4)
- "Green" Molding Compound (No Br, Sb)
- Qualified to AEC-Q101 Standards for High Reliability

## **Mechanical Data**

- Case: PowerDI<sup>®</sup>123
- Case Material: Molded Plastic, "Green" Molding Compound.
  UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Terminal Connections: Cathode Band
- Terminals: Finish Matte Tin annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 @3
- Marking Information: See Page 2
- Ordering Information: See Page 2
- Weight: 0.01 grams (approximate)



## **Maximum Ratings** @T<sub>A</sub> = 25°C unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load. For capacitance load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V <sub>RRM</sub> V <sub>R</sub> WM	30	V
RMS Reverse Voltage	V <sub>R(RMS)</sub>	21	V
Average Forward Current	I <sub>F(AV)</sub>	2.0	А
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I <sub>FSM</sub>	75	А

## Thermal Characteristics

Characteristic	Symbol	Тур	Max	Unit
Thermal Resistance Junction to Soldering Point (Note 2)	$R_{ heta JS}$	_	6	°C/W
Operating Temperature Range	TJ	-65 to +150		
Storage Temperature Range	T <sub>STG</sub>	-65 to	+150	°C

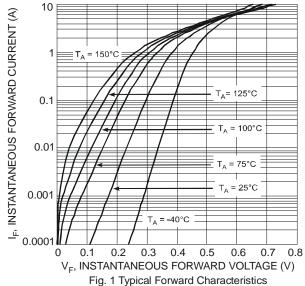
### **Electrical Characteristics** @T<sub>A</sub> = 25°C unless otherwise specified

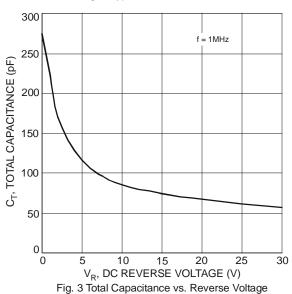
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 3)	V <sub>(BR)R</sub>	30	_	_	V	I <sub>R</sub> = 200μA
Forward Voltage	V <sub>F</sub>	_	_	0.45 0.375	V	I <sub>F</sub> = 2A, T <sub>J</sub> = 25°C I <sub>F</sub> = 2A, T <sub>J</sub> = 125°C
Leakage Current (Note 3)	I <sub>R</sub>	_	<u> </u>	0.200 15	mA	$V_R = 30V, T_J = 25$ °C $V_R = 30V, T_J = 100$ °C
Total Capacitance	Ст	_	85	_	pF	$V_R = 10V, f = 1.0MHz$

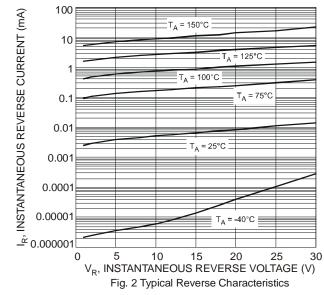
Notes:

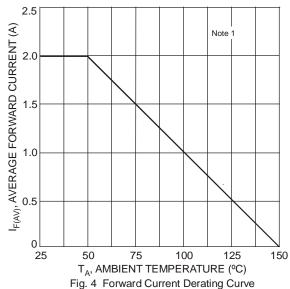
- Part mounted on FR-4 board with 2 oz., minimum recommended copper pad layout, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf. T<sub>A</sub> = 25°C
- $2. \ \, \text{Theoretical $R_{\text{OJS}}$ calculated from the top center of the die straight down to the PCB/cathode tab solder junction. }$
- 3. Short duration pulse test used to minimize self-heating effect.
- 4. EU Directive 2002/95/EC (RoHS). All applicable RoHS exemptions applied, see EU Directive 2002/95/EC Annex Notes.











## Ordering Information (Note 5)

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Part Number	Case	Packaging
DFLS230LH-7	PowerDI <sup>®</sup> 123	3000/Tape & Reel

Notes: 5. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

# **Marking Information**



F03H = Product Type Marking Code YM = Date Code Marking

Y = Year (ex: T = 2006)

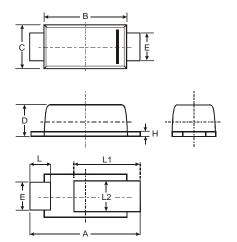
M = Month (ex: 9 = September)

Date Code Key

Date Code Key												
Year	2005	,	2006	2007	7	2008	2009		2010	2011		2012
Code	S		T	U		V	W		Χ	Υ		Z
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

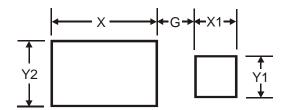


## **Package Outline Dimensions**



PowerDI <sup>®</sup> 123					
Dim	Min	Max	Тур		
Α	3.50	3.90	3.70		
В	2.60	3.00	2.80		
O	1.63	1.93	1.78		
ם	0.93	1.00	0.98		
Е	0.85	1.25	1.00		
Η	0.15	0.25	0.20		
J	0.55	0.75	0.65		
L1	1.80	2.20	2.00		
L2	0.95	1.25	1.10		
All Dimensions in mm					

# **Suggested Pad Layout**



Dimensions	Value (in mm)
G	1.0
X1	2.2
X2	0.9
Y1	1.4
Y2	1.4

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